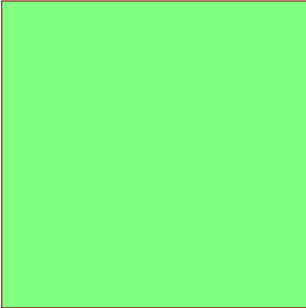
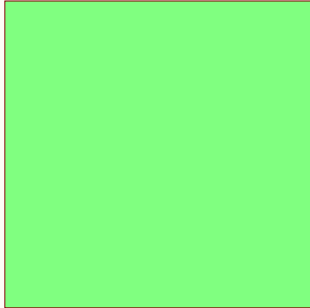


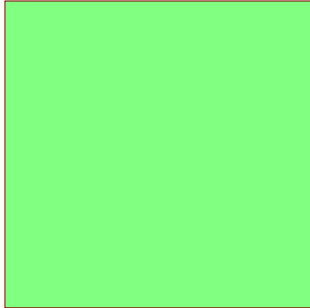
Designator
[01] - COVER PAGE.SchDoc



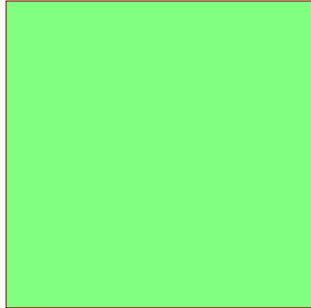
Designator
[02] - BLOCK DIAGRAM.SchDoc



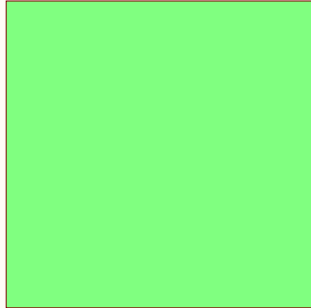
Designator
[03] - MCU.SchDoc



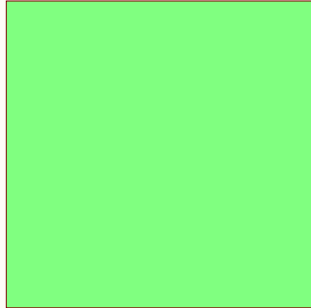
Designator
[04] - IOLINK.SchDoc



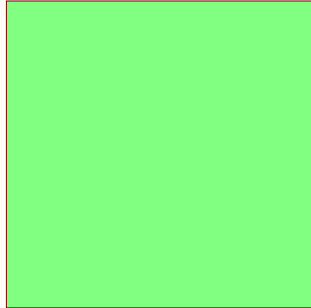
Designator
[05] - MEMORIES.SchDoc



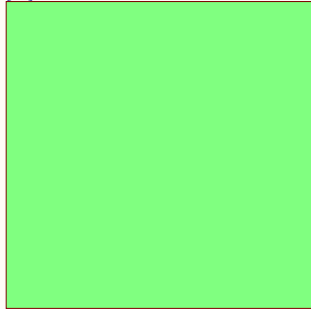
Designator
[06] - RFID.SchDoc



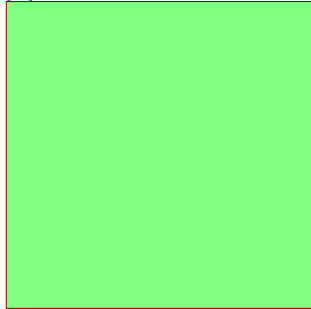
Designator
[07] - SIGNALING.SchDoc



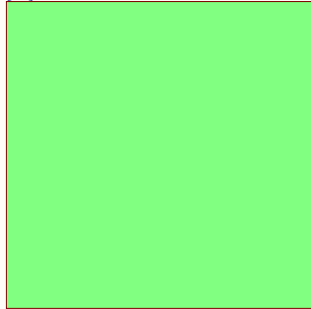
Designator
[08] - POWER.SchDoc



Designator
[09] - CONNECTORS.SchDoc



Designator
[10] - DOC REVISION HISTORY.SchDoc



TEMPLATE NOTES

Set Project Parameters

- 1) Go to Project -> Project Options -> Parameters
- 2) Set Project and Version

Mark Not Fitted Components as

NF

Net Class Example



Differential signal example



TITLE Examples (You can change the color to reflect your company color)

PAGE TITLE

Peripheral / Group of component title

Smaller Ttitle

Schematic Status Explanation

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There should not be any mistakes. Tell the engineer if you find one.

RELEASED - A board with this schematic has been sent to production.

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Variant: [00] - Board All Populated

03/10/2024
V0

DRAFT

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3	MCU	13		23		33	
4	RS232	14		24		34	
5	MEMORIES	15		25		35	
6	RFID	16		26		36	
7	SIGNALING	17		27		37	
8	POWER	18		28		38	
9	CONNECTORS	19		29		39	
10	DOC REVISION HISTORY	20		30		40	

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational
design notes .

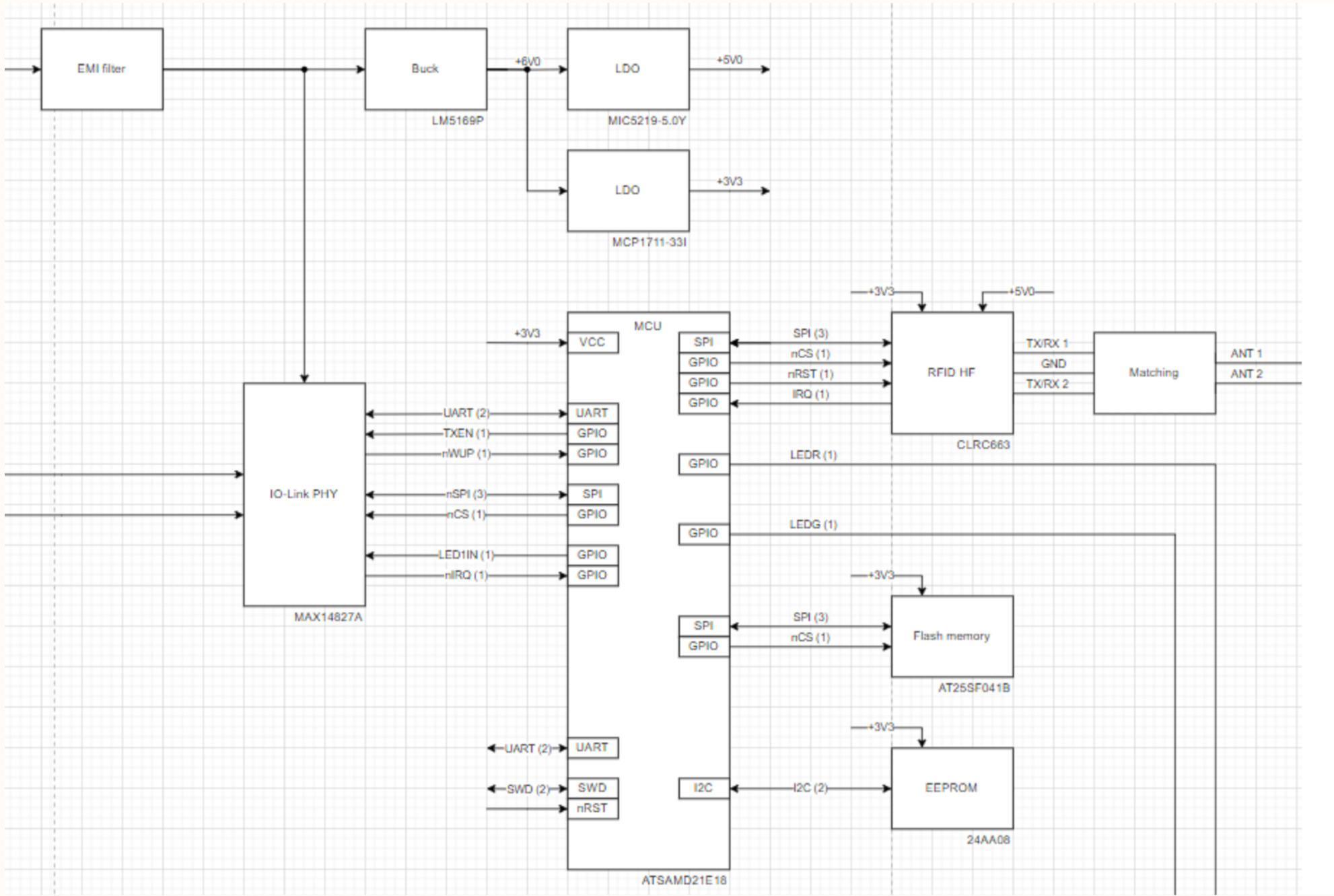
DESIGN NOTE:
Example text for critical
design notes.

DESIGN NOTE:
Example text for cautionary
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

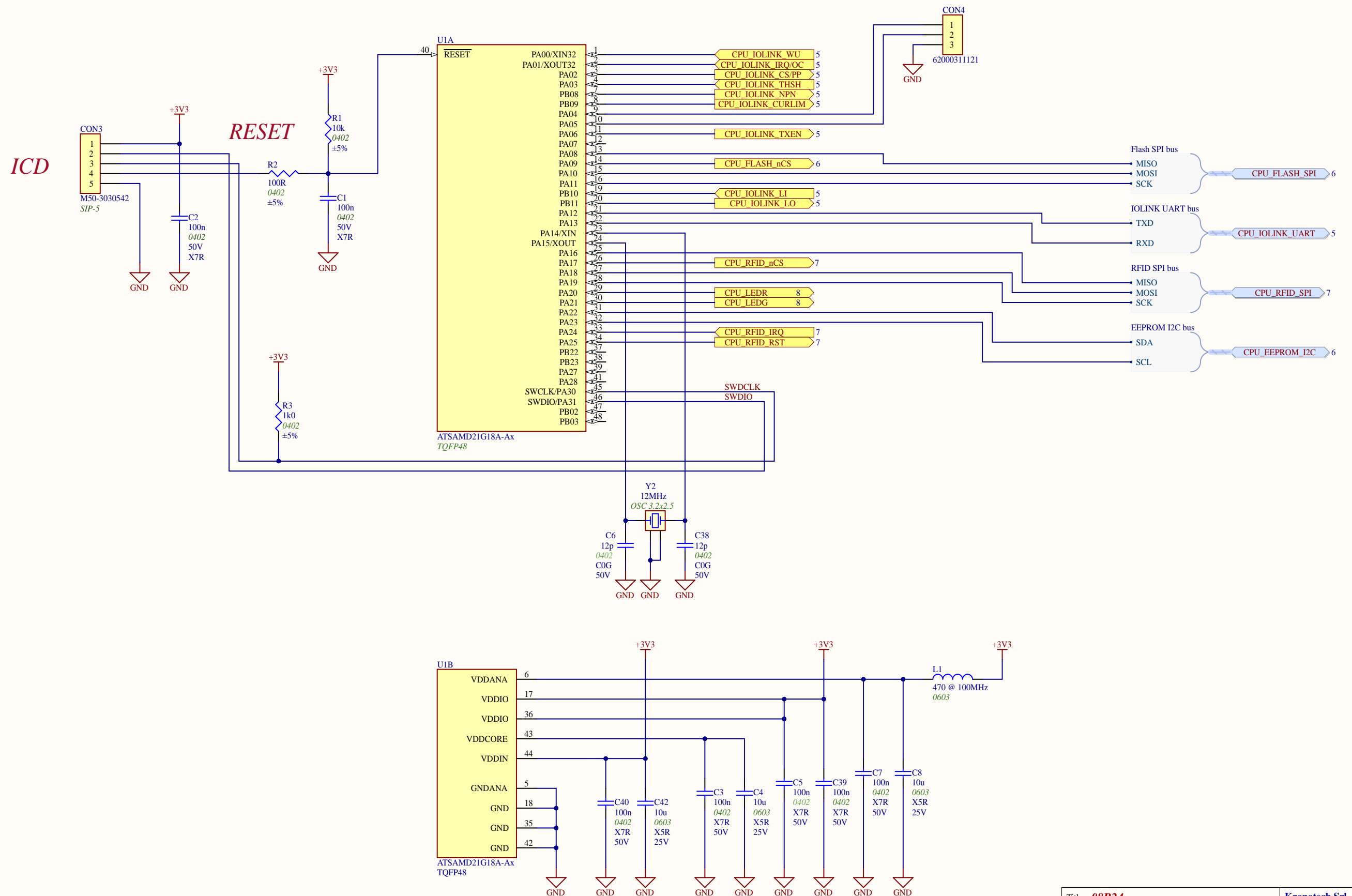
08B24

(Block Diagram)



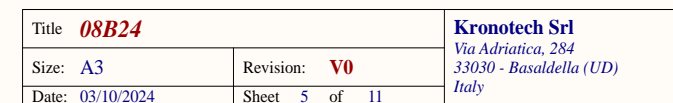
08B24

(MCU)



Title 08B24		Kronotech Srl Via Adriatica, 284 33030 - Basaldella (UD) Italy
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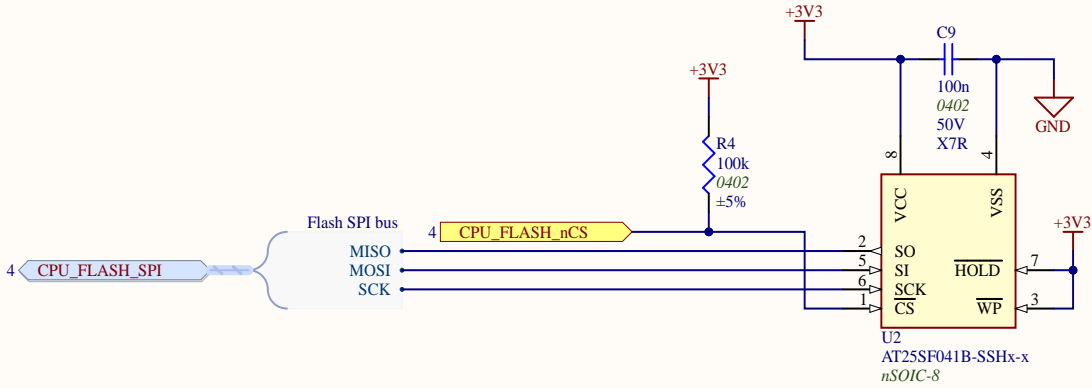
IO_LINK



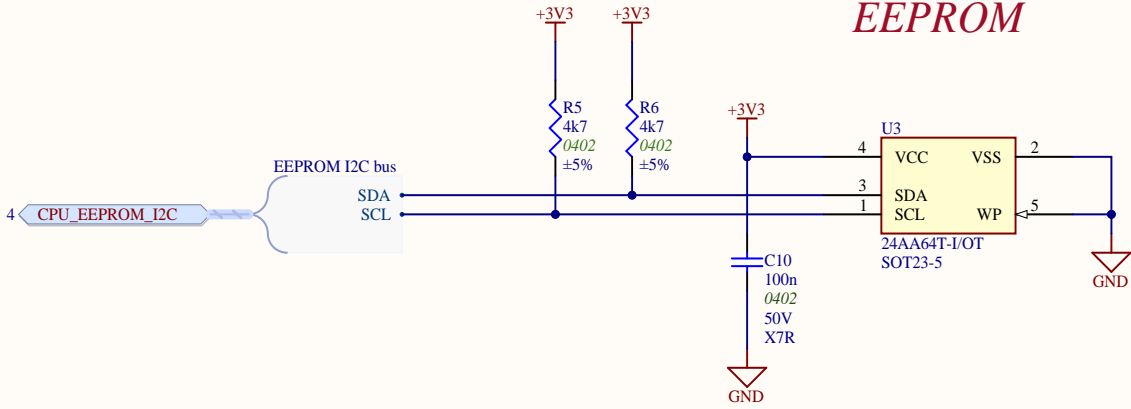
08B24

(Memories)

FLASH MEMORY

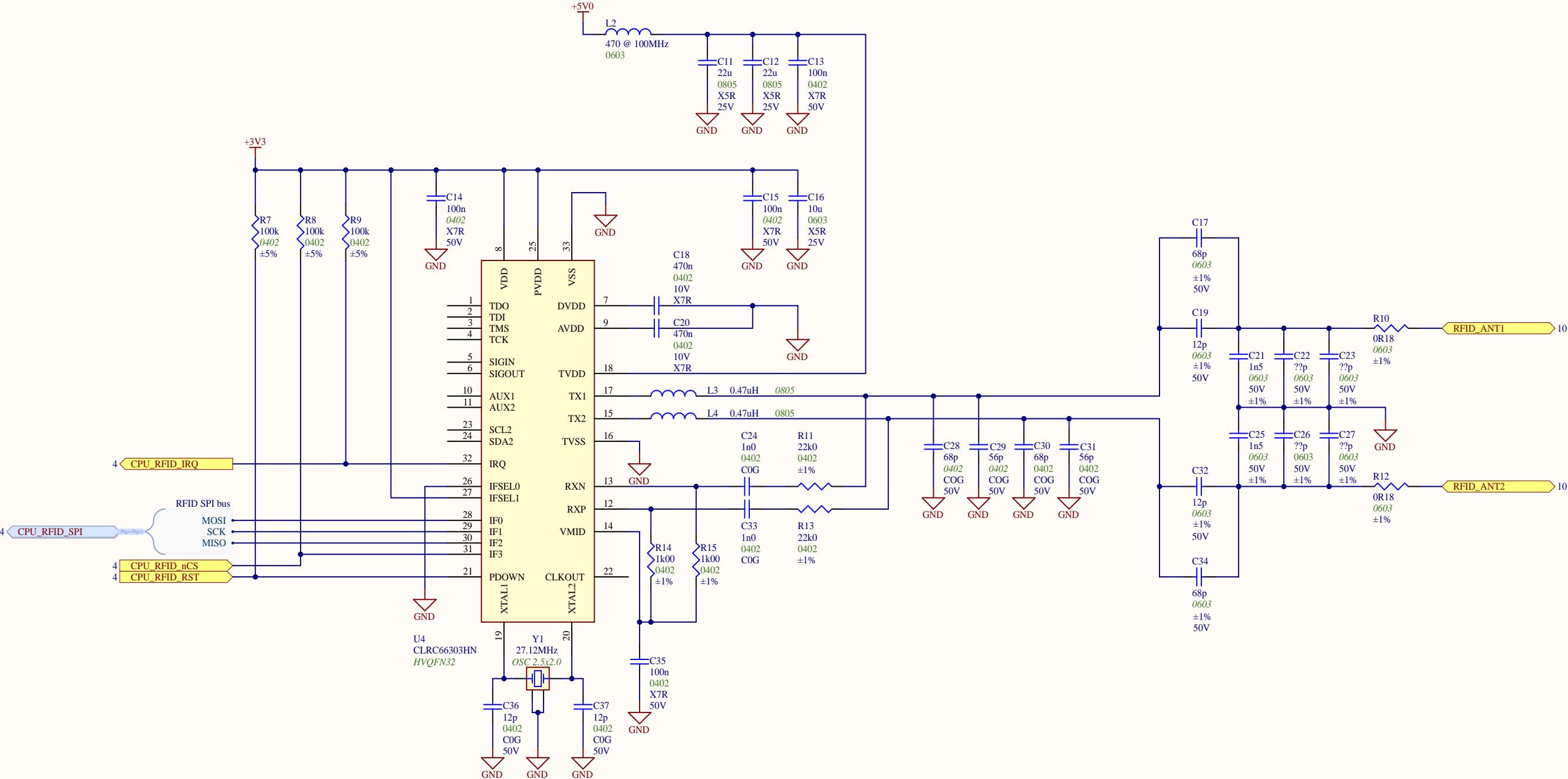


EEPROM



08B24

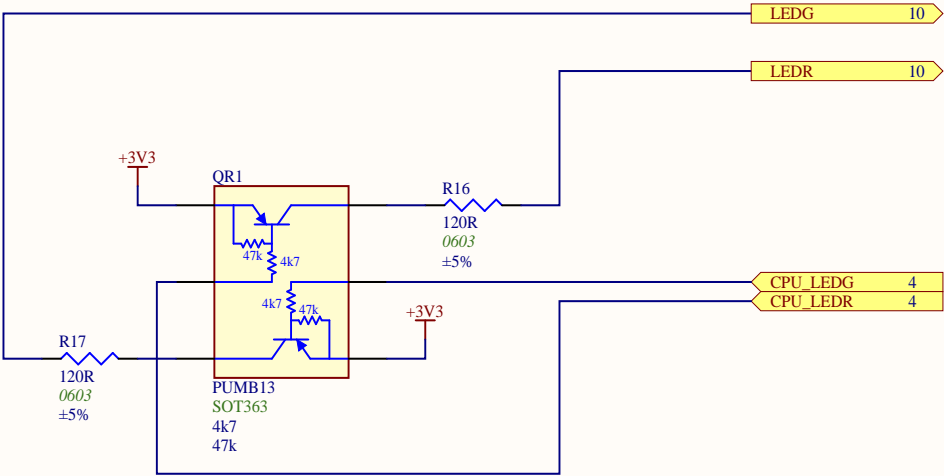
(RFID)



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(Signaling)

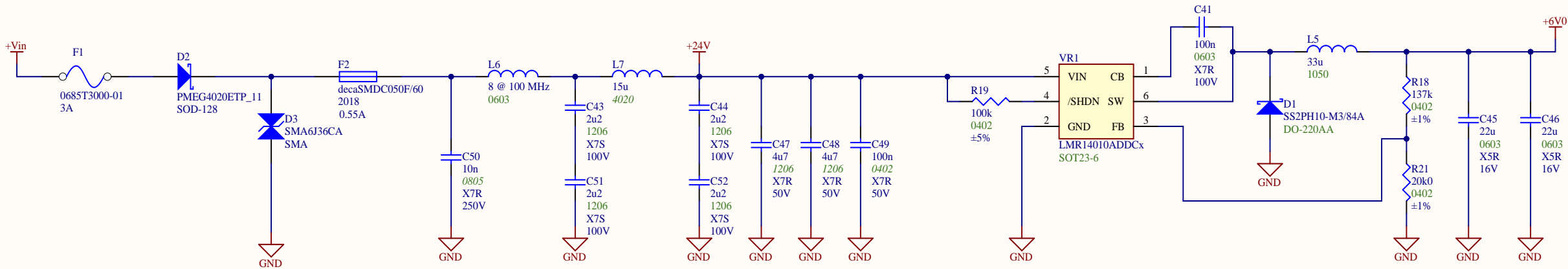
LEDS



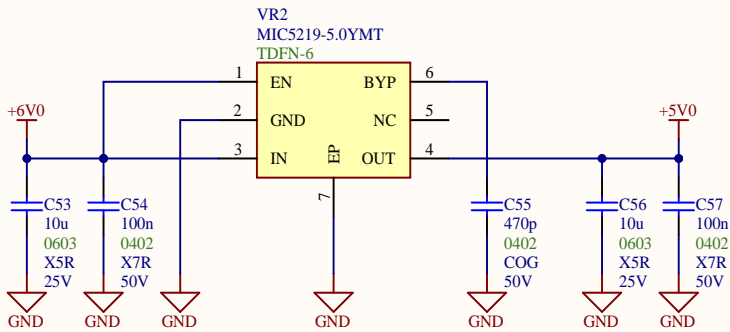
08B24

(Power)

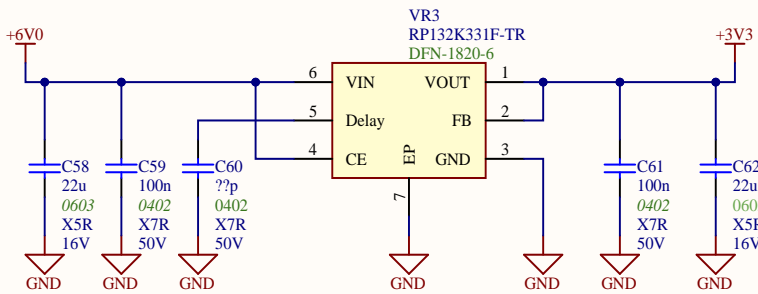
6V0



5V0

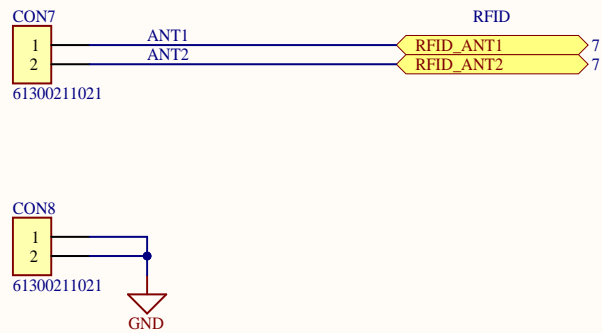
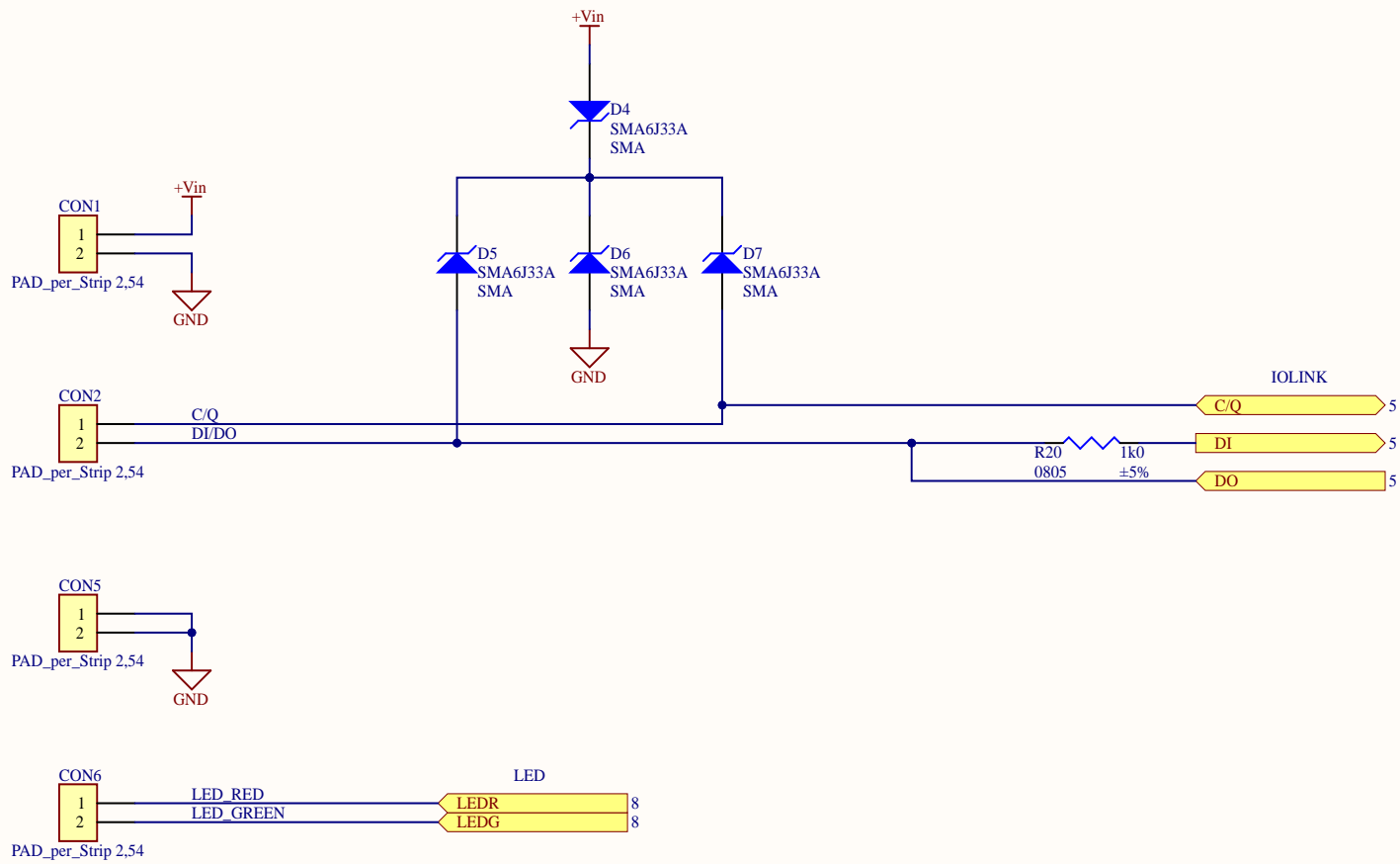


3V3



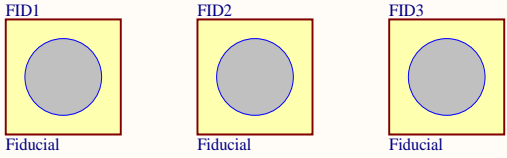
08B24

(Connectors)

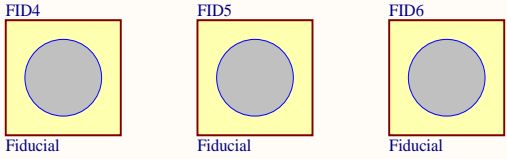


FIDUCIALS

TOP



BOT



08B24

(Doc Revision History)

- 16/05/2024 - 08A24 - Initial release
- 03/10/2024 - 08B24 - MCU U1A pins: SWDIO/CLK moved from uc pins PB02/PB03 to PA30/PA31. BLOCK DIAGRAM: changed 6V5 > 6V0 (VR1 Vout).
- Antenna matching values defined: C17, C34:68pF; C19, C32: 12pF; C21, C25: 1.5nF; R10, R12:0.18Ohm.
 - Remaining ?? components set as DNP on Variant [01] - Sample Production ([06] RFID C22, C23, C26, C27, [08] POWER C60).

CLOCKS (CPU & PCIe)

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